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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Hsiao, Tommy C.; Hui, Angela T.; Ogle, Robert B.; Pham, Tuan ~~Duy~~  
Plat, Marina V.; Ramsbey, Mark T.; Shen, Lewis

Assignee: Advanced Micro Devices, Inc.

Title: Polished Flash Process With Metal Gates And Improved Planarity

Serial No.: 09/430,366 Filing Date: October 28, 1999

Examiner: J. Chen Group Art Unit: 2813

Docket No.: M-7523 US



TC 28  
San Jose, California  
January 14, 2002  
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**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97(c) WITH FEE**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicant(s) wish to call the following documents (copies enclosed) to the attention of the Examiner for the above-identified patent application. The documents were cited in the for the corresponding Application No. :.

A form PTO-1449 listing these documents is enclosed.

The relevance of any non-English language documents cited herein is explained in the accompanying copy of the .

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or

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SKJERVEN MORRILL  
MacPHERSON LLP

25 METRO DRIVE  
SUITE 700  
SAN JOSE, CA 95110  
(408) 453-9200  
FAX (408) 453-7979

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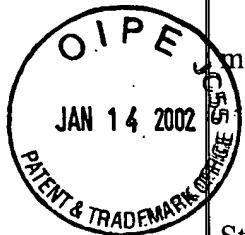
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3. an admission that the information cited herein is, or is considered to be,



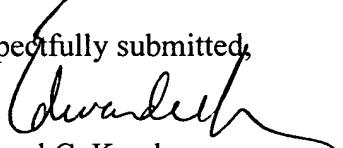
material to patentability as defined in § 1.56(b).

Please charge Deposit Account No. 19-2386 \$180.00 for the Information Disclosure Statement fee under 37 C.F.R. § 1.17(p). The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 19-2386. This paper is being submitted in duplicate.

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Respectfully submitted,

  
Edward C. Kwok  
Attorney for Applicants  
Reg. No. 33,938

LAW OFFICES OF  
SKJERVEN MORRILL  
MacPHERSON LLP

25 METRO DRIVE  
SUITE 700  
SAN JOSE, CA 95110  
(408) 453-9200  
FAX (408) 453-7979

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